





## PNP SURFACE MOUNT TRANSISTOR

#### **Features**

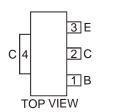
- **Epitaxial Planar Die Construction**
- Ideally Suited for Automated Assembly Processes
- Ideal for Medium Power Switching or Amplification Applications
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)

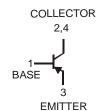
### **Mechanical Data**

- Case: SOT89-3L
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish Matte Tin annealed over Copper leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Marking & Type Code Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.072 grams (approximate)









Schematic and Pin Configuration

## **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	-30	V
Collector-Emitter Voltage	$V_{CEO}$	-20	V
Emitter-Base Voltage	V <sub>EBO</sub>	-6	V
Peak Pulse Current	I <sub>CM</sub>	-10	A
Continuous Collector Current	Ic	-5	А

### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3) @ T <sub>A</sub> = 25°C	P <sub>D</sub>	1	W
Thermal Resistance, Junction to Ambient Air (Note 3) @ T <sub>A</sub> = 25°C	$R_{ heta JA}$	125	°C/W
Operating and Storage Temperature Range	T <sub>j</sub> , T <sub>STG</sub>	-55 to +150	°C

## Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

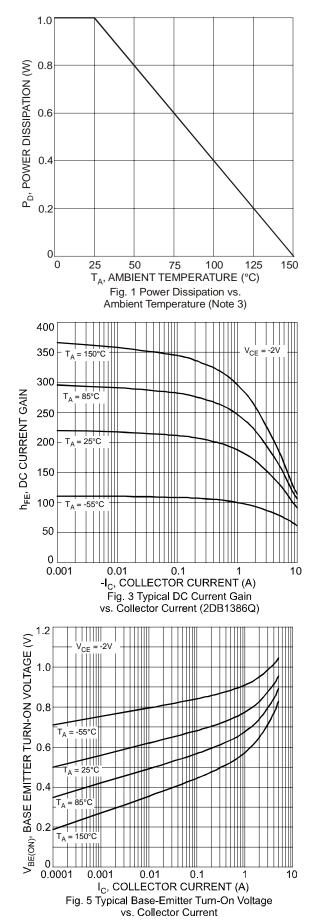
Charac	teristic	Symbol	Min	Тур	Max	Unit	Conditions
OFF CHARACTERISTICS (N	ote 4)						•
Collector-Base Breakdown Vo	ltage	V <sub>(BR)CBO</sub>	-30	_	_	V	$I_C = -50 \mu A, I_E = 0$
Collector-Emitter Breakdown \	/oltage	V <sub>(BR)CEO</sub>	-20	_	_	V	$I_C = -1 \text{mA}, I_B = 0$
Emitter-Base Breakdown Volta	age	V <sub>(BR)EBO</sub>	-6	_	_	V	$I_E = -50\mu A, I_C = 0$
Collector Cut-Off Current		I <sub>CBO</sub>	_	_	-0.5	μА	$V_{CB} = -20V, I_{E} = 0$
Emitter Cut-Off Current		I <sub>EBO</sub>	_	_	-0.5	μΑ	$V_{EB} = -5V, I_{C} = 0$
ON CHARACTERISTICS (Note 4)							
Collector-Emitter Saturation V	oltage	V <sub>CE(SAT)</sub>		-0.25	-1.0	V	$I_C = -4A$ , $I_B = -0.1A$
DC Current Gain	2DB1386Q	hee	120	_	270	_	I <sub>C</sub> = -0.5A, V <sub>CE</sub> = -2V
	2DB1386R		180	_	390		
SMALL SIGNAL CHARACTERISTICS							
Output Capacitance		C <sub>obo</sub>		55	_	pF	$V_{CB} = -20V, I_{E} = 0,$ f = 1MHz
Current Gain-Bandwidth Prod	uct	f <sub>T</sub>	_	100	_	pF	$V_{CE} = -6V, I_{E} = 50mA,$ f = 30MHz

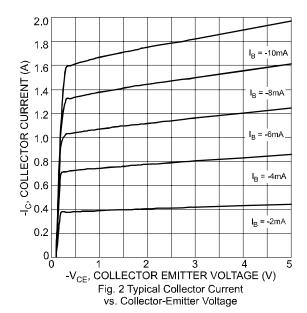
Notes:

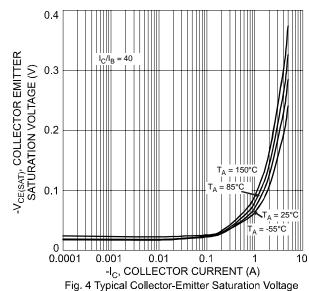
- No purposefully added lead.
- Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php.

  Device mounted on FR-4 PCB; pad layout as shown on page 4 or in Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- 4. Measured under pulsed conditions. Pulse width = 300μs. Duty cycle ≤2%.



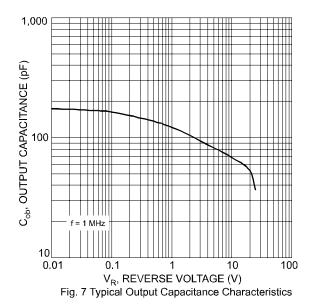


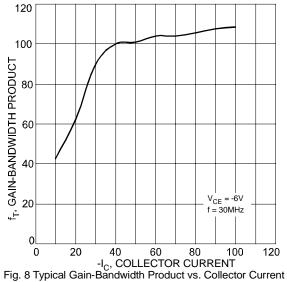




vs. Collector Current





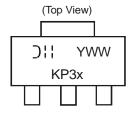


## **Ordering Information** (Note 5)

Device	Packaging	Shipping
2DB1386Q-13	SOT89-3L	2500/Tape & Reel
2DB1386R-13	SOT89-3L	2500/Tape & Reel

5. For packaging details, go to our website at http://www.diodes.com/ap02007.pdf. Notes:

# **Marking Information**

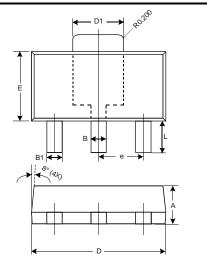


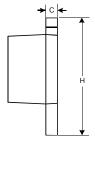
KP3x = Product Type Marking Code, KP3Q = 2DB1386Qwhere:

KP3R = 2DB1386R

YWW = Date Code Marking Y = Last digit of year ex: 7 = 2007 WW = Week code 01 - 52

# **Package Outline Dimensions**

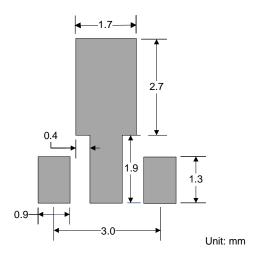




SOT89-3L					
Dim	Min	Max	Тур		
Α	1.40	1.60	1.50		
В	0.45	0.55	0.50		
B1	0.37	0.47	0.42		
С	0.35	0.43	0.38		
D	4.40	4.60	4.50		
D1	1.50	1.70	1.60		
E	2.40	2.60	2.50		
е	_	_	1.50		
Н	3.95	4.25	4.10		
L	0.90	1.20	1.05		
All Dimensions in mm					



# **Suggested Pad Layout**



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